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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Hisashi OHTANI et al.

Serial No. 09/837,552

Filed: April 19, 2001

For: SEMICONDUCTOR DEVICE AND METHOD THEREOF)

Examiner: Andrew Richards

Group Art Unit: 2815

#29
IDS
J. Macmillan
1/28/04

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

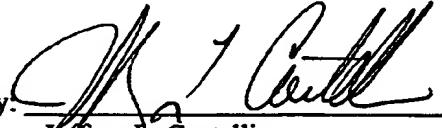
The documents are being submitted within three (3) months of the filing of this application or entry into the national stage of this application, or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

Applicant's wish to bring to the Examiner's attention that Publication Nos. US-2001/0030722; US-2002/0080295; and U.S. Patent No. 6,323,068 were cited in related application Serial No. 09/837,877. In addition, U.S. Patent Nos. 6,479,333 and 6,603,453 were cited in related application Serial No. 09/837,558.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initial a copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

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